

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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HB  
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Applicant : Gilbert M. Wolrich et al. Art Unit : Unknown  
 Serial No. : Not Yet Assigned Examiner : Unknown  
 Filed : March 16, 2001  
 Title : **LOCAL REGISTER INSTRUCTION FOR MICRO ENGINE USED IN MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE**

Commissioner for Patents  
 Washington, D.C. 20231

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

The Title:

Please amend the title throughout to read:

**"REGISTER INSTRUCTIONS FOR A MULTITHREADED PROCESSOR"**

In the specification:

On page 11, between lines 6-17, please add the following paragraph to the 'Detailed Description':

N.E. A1  
 -- The EBOX also includes two multiplexors, "mux A" and "mux B", which provide a selection capability of the operands to be input to ALU 76a. The 32-bit wide output buses from several microengine data sources are connected to mux A and mux B, including an IMMEDIATE\_DATA bus from the control logic 72, a write-back data bypass bus from the

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